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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/607,782	06/30/2000	Douglas Raymond Parker	042390.P8837	9577
7590 10/05/2004		EXAMINER		
Jan Carol Little			TON, MY TRANG	
Blakely Sokoloff Taylor & Zafman LLP			ART UNIT	PAPER NUMBER
Seventh Floor 12400 Wilshire Boulevard				FAFER NUMBER
Los Angeles, CA 90025-1026			2816	
			DATE MAILED: 10/05/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Comments		Application No.	Applicant(s)				
		09/607,782	PARKER ET AL.				
	Office Action Summary	Examiner	Art Unit				
		My-Trang N. Ton	2816				
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the	correspondence address				
THE I - Exter after: - If the - If NO - Failur Any re	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply operiod for reply is specified above, the maximum statutory period were to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing end patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be t ly within the statutory minimum of thirty (30) da will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDON	timely filed ays will be considered timely. m the mailing date of this communication. IED (35 U.S.C. § 133).				
Status							
1)⊠	Responsive to communication(s) filed on 09 Ju	<u>uly 2004</u> .					
2a)□	This action is FINAL . 2b)⊠ This	s action is non-final.					
3)	3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments						
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Dispositi	ion of Claims						
5)⊠ 6)⊠	• • • • • • • • • • • • • • • • • • • •	• •					
· · · · · · · · · · · · · · · · · · ·	Claim(s) is/are objected to. Claim(s) are subject to restriction and/or election requirement.						
Application	on Papers						
10) 🖾 -	The specification is objected to by the Examine The drawing(s) filed on <u>09 July 2004</u> is/are: a)[Applicant may not request that any objection to the oreal Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	☑ accepted or b)☐ objected to drawing(s) be held in abeyance. So tion is required if the drawing(s) is o	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1.121(d).				
Priority u	ınder 35 U.S.C. § 119						
a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority documents application from the International Bureau see the attached detailed Office action for a list	s have been received. s have been received in Applicantly documents have been received in Received in PCT Rule 17.2(a)).	ntion Noved in this National Stage				
Attachment	i(s)						
Notice Notice Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summar Paper No(s)/Mail D 5) Notice of Informal 6) Other:					

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DETAILED ACTION

In response to Applicant's argument filed on 7/09/04, the rejection made in the last office action on the Bohr et al reference is withdrawn. A new Office action has been made as follows:

Claim Rejections - 35 USC § 112

Claims 21-24 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 21, it is not clearly understood what is meant by "having a trip point to track a potential on the output node". For example, when and how does "trip point to track a potential on the output node" actually occur? This appears to imply the trip point actually varies with respect to the output potential. However, using the applicant's own Fig. 2 as a reference, how is the trip point of 204 actually changed? As seen in that figure, gain stage 204 would appear to have a SET trip point condition when in normal operation. Thus, the limitation "a gain stage, coupled to the amplifier, having a trip point to track a potential on the output node" is considered misdescriptive.

Claims 22-24 are rendered indefinite by the deficiencies of claim 21.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

⁽b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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After further consideration, the previous rejection under Applicant's Prior Art

(Figure 1) is now included. It appears that rejection was inadvertently overlooked. It is

now believed these claims are still rejected over Applicant's Prior Art, Figure 1.

Claims 21-22 and 24 remain rejected under 35 U.S.C. 102(b) as being anticipated by the prior art depicted by Applicant's Fig. 1.

The prior art, Fig. 1 disclose a fuse sensing circuit including:

an amplifier (102) having an output node (120); and

a gain stage (104), coupled to the amplifier (102), having a trip point to track a potential on the output node (it is well known to one of ordinary skill in the art that a CMOS inverter (one known type of a gain stage) has a trip point. Since input of the gain stage (104) directly connected to the output 120 of the amplifier 102, the potential on output node so clearly tracks the amplifier output node potential) as recited in claim 1.

Regarding claim 22: the amplifier (102) comprising a sense branch (105) coupled to a reference branch (102) in a current mirror configuration.

Regarding claim 24: first and second voltages (132, 130) each coupled to the amplifier and the gain stage.

Claims 21-24 are rejected under 35 U.S.C. 102(b) as being anticipated by Johnson (U.S Patent No. 6,081,162).

Johnson discloses in fig. 1 a circuit including:

an amplifier (120) having an output node (connected to output of 120); and a gain stage (128), coupled to the amplifier (120), having a trip point to track a potential on the output node (col. 3, line 36-39 discloses that amplifier 120 operates in a

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closed control loop to ensure that its inverting input 117 exactly tracks the trip point signal at its non-inverting input) as recited in claim 1.

Regarding claim 22: the amplifier (120) comprising a sense branch (i17, i19) coupled to a reference branch (i16, i20) in a current mirror configuration (see Fig. 3).

Regarding claim 23: the gain stage (128) comprising a scaled replica (i3, i1, i2, Fig. 2) of the reference branch (i16, i20, i26) or the sense branch (i17, i19, i26, Fig. 3).

Regarding claim 24: first and second voltages (Vdd and Ground) each coupled to the amplifier and the gain stage.

Allowable Subject Matter

Claims 1-9 are allowable over the prior art of record.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 26, 2004

MY-TRANG NUTON PRIMARY EXAMINER